

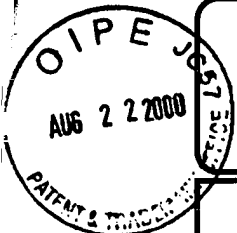
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FAU 2814 #

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## CONTINUED PROSECUTION APPLICATION (CPA) REQUEST TRANSMITTAL

CHECK BOX, if applicable:

☐ DUPLICATE

Submit an original, and a duplicate for fee processing.

(Only for Continuation or Divisional applications under 37 CFR 1.53(d))

# 13/CPA  
8/31/00  
V. Varnall

Address to:

Assistant Commissioner for Patents  
Box CPA  
Washington, DC 20231

Attorney Docket No.	042390.P6643
First Named Inventor	Brian D. Possley
Examiner Name	N. Ngo
Group/Art Unit	2814
Express Mail Label No.	EL034434607US

This is request for a ☒ continuation or ☐ divisional application under 37 CFR 1.53(d), (continued prosecution application (CPA)) of prior application number 09/262,458, filed on March 4, 1999, entitled GATE ARRAY ARCHITECTURE.

### NOTES

**FILING QUALIFICATIONS:** The prior application identified above must be a nonprovisional application that is either: (1) complete as defined by 37 CFR 1.51(b), or (2) the national stage of an international application in compliance with 35 U.S.C. 371. A Notice will be placed on a patent issuing from a CPA, except for reissues and designs, to the effect that the patent issued on a CPA and is subject to the twenty-year patent term provisions of 35 U.S.C. § 154(a)(2). Therefore, the prior application of a CPA may have been filed before, on or after June 8, 1995.

**C-I-P NOT PERMITTED:** A continuation-in-part application cannot be filed as a CPA under 37 CFR § 1.53(d), but must be filed under 37 CFR 1.53(b).

**EXPRESS ABANDONMENT OF PRIOR APPLICATION:** The filing of this CPA is a request to expressly abandon the prior application as of the filing date of the request for a CPA. 37 CFR § 1.53(b) must be used to file a continuation, divisional, or continuation-in-part of an application that is not to be abandoned.

**ACCESS TO PRIOR APPLICATION:** The filing of this CPA will be construed to include a waiver of confidentiality by the applicant under 35 U.S.C. 122 to the extent that any member of the public who is entitled under the provisions of 37 CFR § 1.14 to access to, copies of, or information concerning, the prior application may be given similar access to, copies of, or similar information concerning, the other application or applications in the file jacket.

**35 U.S.C. 120 STATEMENT:** In a CPA, no reference to the prior application is needed in the first sentence of the specification and none should be submitted. If a sentence referencing the prior application is submitted, it will not be entered. A request for a CPA is the specific reference required by 35 U.S.C. 120 and to every application assigned the application number identified in such request, 37 CFR § 1.78(a).

- ☐ Enter the unentered amendment previously filed on \_\_\_\_\_ under 37 CFR § 1.116 in the prior nonprovisional application.
- ☒ A preliminary amendment is enclosed.
- This application is filed by fewer than all the inventors named in the prior application, 37 CFR § 1.53 (d)(4).
  - ☐ **DELETE** the following inventor(s) named in the prior nonprovisional application:  
\_\_\_\_\_
  - ☐ The inventor(s) to be deleted are set forth on a separate sheet attached hereto.
- ☐ A new power of attorney or authorization of Agent (PTO/SB/81) is enclosed.
- Information Disclosure Statement (IDS) is enclosed:
 

- ☐ PTO-1449
  - ☐ Copies of IDS Citations

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[Page 1 of 2]

Burden Hour Statement: This form is estimated to take 0.4 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Box CPA, Washington, DC 20231.

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PTO/SB/29 (12/97)  
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CLAIMS	(1) FOR	(2) NUMBER FILED	(3) NUMBER EXTRA	(4) RATE	(5) CALCULATIONS
TOTAL CLAIMS (37 CFR § 1.16(c))		17 -20* =	0	x \$ 18.00 =	\$
INDEPENDENT CLAIMS(37 CFR § 1.16(b))		2 -3** =	0	x \$ 78.00 =	
MULTIPLE DEPENDENT CLAIMS (if applicable) (37 CFR 1.16(d))				+\$ _____ =	
				BASIC FEE (37 CFR § 1.16)	690.00
				Total of above Calculations =	\$690.00
Reduction by 50% for filing by small entity (Note 37 CFR §§ 1.9, 1.27, 1.28).					
				TOTAL =	\$690.00

6. Small entity status:

- a. ☐ A small entity statement is enclosed.  
b. ☐ A small entity statement was filed in the prior nonprovisional application and such status is still proper and desired.  
c. ☐ Is no longer claimed.

7. The Commissioner is hereby authorized to credit overpayments or charge the following fees to Deposit Account No. 02 2666:

- a. ☒ Fees required under 37 CFR § 1.16.  
b. ☒ Fees required under 37 CFR § 1.17.  
c. ☒ Fees required under 37 CFR § 1.18.

8. ☒ A check in the amount of \$ 690.00 is enclosed.

9. ☐ Other: \_\_\_\_\_

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**NOTE:**

The prior application's correspondence address will carry over to this CPA UNLESS a new correspondence address is provided below.

**10. NEW CORRESPONDENCE ADDRESS**

☐ Customer Number of Bar Code Label

or ☒ Correspondence address below

(Insert Customer No. or Attach bare code label here)

Name	Gregory D. Caldwell, Reg. No. 39,926				
	BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP				
Address	12400 Wilshire Boulevard, Seventh Floor				
City	Los Angeles	State	California	Zip Code	90025
Country	U.S.A.	Telephone	(503) 684-6200	Fax	(503) 684-3245

**11. SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED**

NAME	Gregory D. Caldwell, Reg. No. 39,926
SIGNATURE	
DATE	August 22, 2000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re CPA of:

Possley

Application No.: 09/262,458

Filed: March 4, 1999

For: Gate Array Architecture



Examiner: N. Ngo

Art Unit: 2814

# 14/c  
8/31/00  
V. Jannell

PRELIMINARY AMENDMENT

BOX CPA  
ASSISTANT COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

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Dear Sir:

For the above continued prosecution application, please enter the following amendments and consider the following remarks:

IN THE CLAIMS:

(once amended) 1. An integrated circuit comprising: a gate array architecture;  
said gate array architecture including a semiconductor substrate having a plurality of N-type diffusion regions and P-type diffusion regions; said diffusion regions having partially overlying polysilicon landing sites to form N-type and P-type transistors;  
wherein the regions are relatively-sized to form two distinct transistor sizes, smaller N- and P-type transistors and larger N- and P-type transistors[.];  
successive rows of small diffusion regions are followed by successive rows of regular-sized diffusion regions; and  
immediately successive rows within similarly-sized diffusion regions have opposite polarity.

Please cancel claim 12.